



Implementing Monitors

- Can we use semaphores to implement condition variables?
- Simple attempt:

```
Wait() { semaphore->P(); } Signal() { semaphore->V(); }
```

Solution is not relinquishing the lock:

lock.Acquire();
while (lcondition)
Wait();
lock.Release();

lock.Acquire();
Signal();
lock.Release();



Second Attempt

- Use one semaphore for each condition variable
- Release the lock during wait:

```
Wait(Lock *lock) {
  lock->Release();
  semaphore->P();
  lock->Acquire();
}
Signal() { semaphore->V(); }
```

Is this solution correct?



Peek at the waiting queue

Perform a check during signal:
Wait(Lock *lock) {
 lock->Release();
 semaphore->P();
 lock->Acquire();
}
Signal()
{
 if semaphore queue is not empty
 semaphore->V();

• Well, it is cheating! But is it correct?



Implementing Monitors

Using one semaphore for each waiting thread --- making sure it indeed gets the message when it is signalled.

class Condition { List waitQueue; }
Condition::Wait(Lock* lock) {
Semaphore *w;
w = new Semaphore (0);
add w to the waitQueue;
lock-Release();
w.>P();
lock->Acquire();
delete w;

Condition::Signal(Lock* lock) {
Semaphore *w;

if anyone on waitQueue {
 Take a waiting element off and name it w;
 w->V();
 }
}



Announcements

- Sign up for design review meetings at the end of class
 - Meetings will take place tomorrow
- Read lottery scheduling paper for Friday
 - No review required



Multiprocessors & Parallel Programs

- Difficulties of developing parallel programs
 - Hard to design & debug
 - Application characteristics might limit parallelism
 - What is the inherent parallelism in the program?Latency of communication and overheads
 - Threads need to communicate with each other
 - Suppose a thread creates a new task is it easier to just execute it than passing the task to a different thread?



Approach #1

- Operating system level approach
- Create kernel threads
 - Communicate priorities to kernel
- Use kernel's communication and synchronization primitives
- But kernel threads are too expensive to create
 - Solution: keep a pool of kernel threads, reuse within application
- Problems:
 - Context switches are still slow
 - Kernel keeps lot more state around and is not aware of user-level program properties
 - Cannot customize the scheduling policy



Approach #2

- One kernel thread for each processor in the system
- Implement user-level threads entirely at the user-level in the runtime system
 - Any user thread can run on any kernel thread
 - Very fast thread creation and context switch
 - Fast synchronization
- · Can support much finer-grained parallelism
- Problem: Two schedulers!
 - What if a task does blocking I/O
 - Loses CPU
 - What if there are other applications running on the machine?
 - Application might lose a kernel thread at a "bad time"
 - Application might sometimes need fewer threads



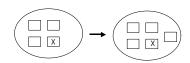
Scheduler Activations

- Mechanism of communicating between the two schedulers
- Scheduler activation:
 - Vessel for running user threads (acts like a kernel thread)
 - · Can think of it as a virtual processor
 - Notifies the user-level runtime system of interesting kernel events
 - Provides space for saving processor context of the currently running user thread when the thread is stopped in the kernel
- Old world: fixed # of kernel threads
 - New world: fixed number of "running" threads



Scenario 1

- Application has certain number of activations running
- If an activation is blocked:
 - New activation is created
 - Allows the user-level scheduler to run in this new activation

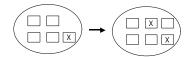


 Runtime scheduler can schedule another user thread to run on the new activation



Scenario 2

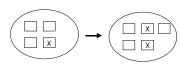
- One of the blocked activations wakes up
- · Kernel notifies the application
 - Preempt another activation
 - Create a new activation and tell the user level scheduler:
 - Previous activation is now unblocked
 - Existing activation has been preempted
 - User level scheduler decides what to run where
 - Has access to all of the register state





Scenario 3

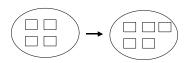
- Kernel takes away an activation
- Notifies the application
- Must preempt activation to report
- Same as before:



User-level scheduler again makes a decision on which threads to run



- New CPU becomes available
- Kernel creates a new activation



- User-level scheduler picks a new thread to execute on the new activation
- In addition, at any point the application can tell kernel it doesn't need extra CPUs



Summary

- Three key features about this approach:
 - Goal is the get user-level threads performance with the scheduling consistency provided by kernel-level threads in multiprocessors
 - The problem to solve: coordinating two independent thread schedulers
 - Scheduler activations used to transmit information between the two as well as to provide virtual processors
- Lesson: export your functionality (in this case, threads) out of the kernel for improved performance and flexibility
 Figure out how to interact with the kernel "just enough"