#### Today **CSE P 504** Recap Abstract interpretation Advanced topics in Software Systems Formal methods • • Primer on solver-aided reasoning Fall 2022 SMTLIB and Z3 0 • Examples **Formal methods** December 05, 2022 HW2 Timing/structure • Logistics of HW2 • Multiple constraints and considerations to balance No homework/in-class during Thanksgiving week No final exam but end-of-quarter grading pressure Two parts and partial overlap with in-class 7 • Part 2 Simplified execution model: 0 CF builds AST and CFG from source code CF traverses the AST and adds type annotations (abstract values) CF calls your implementation when it needs additional information (it calls the transfer functions and the abstraction function) CF traverses the fully annotated AST and calls your implementation for error reporting





#### Abstract interpretation: recap and Q&A

#### Abstract interpretation Q&A

- What remains unclear after consulting the readings, examples, and exercises?
- Any specific roadblocks?
- Any additional thoughts beyond lecture content and hw2?

## A primer on solver-aided reasoning and verification





## What is Z3?

- An SMT (Satisfiability Modulo Theories) solver.
- Uses a standard language (SMT-LIB).
  - Print to the screen.
  - Declare variables and functions.
  - Define constraints.



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#### Which question does this code answer?

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This code is asking the question: Does an integer greater than 0 exist?

# A first example



Does this method ever return 1?





## Reasoning about program equivalence



## Summary

- Solver-aided reasoning is used for testing and verification.
- SMT solvers:
  - $\circ$   $\;$  Provide one solution, if one exists.
  - $\circ$   $\;$  Are commonly used to find counter-examples (or prove <code>unsat</code>).
  - $\circ$   $\;$  Support many theories that can model program semantics.
  - $\circ$   $\:$  Usually support a standard language (SMT-lib).
- The challenge is to model a problem as a constraint system. A few examples:
  - Statistical test selection
  - Data-structure synthesis
  - Program synthesis
- Many higher-level DSLs and language bindings exist.

### In-class 7: formal methods